T.J. Krutsick 10

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Thomas J. Krutsick

Case:

10

Serial No.:

To Be Assigned

Filing Date:

February 1, 2002

Title:

Method of Fabricating Complementary

Self-Aligned Bipolar Transistors

Group:

To Be Assigned

Examiner:

To Be Assigned



INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

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Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449. A copy of each listed document is enclosed.

U.S. Patent Documents

- U.S. Patent No. 6,087,234 issued on 07/11/00 to Wu
- U.S. Patent No. 6,043,126 issued on 03/28/00 to Kinzer
- U.S. Patent No. 5,880,022 issued on 03/09/99 to Lee et al.
- U.S. Patent No. 5,439,833 issued on 08/08/95 to Hebert et al.
- U.S. Patent No. 4,486,943 issued on 12/11/84 to Ryden et al.
- U.S. Patent No. 4,435,896 issued on 03/13/84 to Parrillo et al.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,

Date: February 1, 2002

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